

# Functional correctness of an optimized modular inversion algorithm

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# Context

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# Modular inversion

For a prime  $m$  and  $y \neq 0$ , compute  $y^{-1}$ , such that,  $yy^{-1} = 1(\text{mod } m)$ .

Intensively used in cryptographic algorithm families, e.g. RSA and ECC

- **Efficiency** matters: large  $m$  (e.g.  $2^{255} - 19$ )
- **Security** constraints: constant time

In 2020 Pornin publishes an algorithm with ~25% perf gain w.r.t. best implem Bernstein and Yang (2019).

Used in:

1. **Falcon**: Post-quantum signature scheme (cf NIST PQ Cryptography Standardization)
2. **blst**: Library for performant signature schemes

# Modular inversion via EGCD

```
mod_inv(y, m) :=  
a <- y, b <- m, u <- 1, v <- 0  
while a ≠ 0  
    quotient <- b div a  
    (b, a) <- (a, b - quotient * a)  
    (v, u) <- (u, v - quotient * u)  
return v
```

$$yy^{-1} = 1(\text{mod } m)$$

$$yy^{-1} + mu = 1(\text{mod } m)$$

$$yy^{-1} + mu = \text{gcd}(y, m)$$

(Bezout's identity!)

# **A highly optimized modular inversion via EGCD**

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**Algorithm 2** Extended Binary GCD (optimized algorithm)**Require:** Odd modulus  $m$  ( $m \geq 3$ ,  $m \bmod 2 = 1$ ), value  $y$  ( $0 \leq y < m$ ), and  $k > 1$ **Ensure:**  $1/y \bmod m$  (if  $\text{GCD}(y, m) = 1$ )

```

1:  $a \leftarrow y, u \leftarrow 1, b \leftarrow m, v \leftarrow 0$ 
2: for  $1 \leq i \leq \lceil (2\text{len}(m) - 1)/k \rceil$  do
3:    $n \leftarrow \max(\text{len}(a), \text{len}(b), 2k)$ 
4:    $\bar{a} \leftarrow (a \bmod 2^{k-1}) + 2^{k-1} \lfloor a/2^{n-k-1} \rfloor$   $\triangleright \bar{a} < 2^{2k}$ 
5:    $\bar{b} \leftarrow (b \bmod 2^{k-1}) + 2^{k-1} \lfloor b/2^{n-k-1} \rfloor$   $\triangleright \bar{b} < 2^{2k}$ 
6:    $f_0 \leftarrow 1, g_0 \leftarrow 0, f_1 \leftarrow 0, g_1 \leftarrow 1$ 
7:   for  $1 \leq j \leq k - 1$  do
8:     if  $\bar{a} = 0 \bmod 2$  then
```

**Note.** An initial version of this note used a slightly more aggressive algorithm ( which was wrong. )

```

15:    $(f_0, g_0) \leftarrow (f_0 - f_1, g_0 - g_1)$ 
16:    $(f_1, g_1) \leftarrow (2f_1, 2g_1)$ 
17:    $(a, b) \leftarrow ((af_0 + bg_0)/2^{k-1}, (af_1 + bg_1)/2^{k-1})$ 
18:   if  $a < 0$  then
19:      $(a, f_0, g_0) \leftarrow (-a, -f_0, -g_0)$ 
20:   if  $b < 0$  then
21:      $(b, f_1, g_1) \leftarrow (-b, -f_1, -g_1)$ 
22:    $(u, v) \leftarrow (uf_0 + vg_0 \bmod m, uf_1 + vg_1 \bmod m)$ 
23:  $v \leftarrow v/2^{(k-1)\lceil (2\text{len}(m)-1)/(k-1) \rceil} \bmod m$ 
24: if  $b \neq 1$  then
25:   return 0 (value  $y$  is not invertible)
```

# Binary EGCD

```
bin_mod_inv(y, m) :=  
  a ← y, b ← m, u ← 1, v ← 0,  
  while a ≠ 0  
    if a = 0 mod 2  
      a ← a / 2  
      u ← u / 2 mod m  
    else  
      if a < b  
        swap(a, b)  
        swap(u, v)  
      a ← (a - b) / 2  
      u ← (u - v) / 2 mod m  
  return v
```

## Observe

- No decision is made on values  $u$  and  $v$
- Parity needs only lowest bit
- $a < b$  needs a sensible amount of higher bits

## Possible optimizations?

- Do batch update of  $u$  and  $v$
- Compute on approximations of  $a$  and  $b$

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## Batch update of u and v

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opt_mod_inv (y, m, k) :=  
a <- y, b <- m, u <- 1, v <- 0      // init variables and coeff for a and b  
while a ≠ 0
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## Batch update of $u$ and $v$

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opt_mod_inv (y, m, k) :=  
  a ← y, b ← m, u ← 1, v ← 0      // init variables and coeff for a and b  
  while a ≠ 0  
    R                               <- init_record() // records updates during execution
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# Approximation-based optimization

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# Approximation based modular inversion

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opt_mod_inv (y, m, k) :=  
  a <- y, b <- m, u <- 1, v <- 0           // init variables and coeff for a and b  
  while a ≠ 0  
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opt_mod_inv (y, m, k) :=  
a <- y, b <- m, u <- 1, v <- 0           // init variables and coeff for a and b  
while a  $\neq$  0  
  R                                     <- init_record() // records updates during execution  
  ( $\alpha$ ,  $\beta$ )                         <- approx(a,b,k)  
  do k iters  
  
    ; R <- record_halving_a(R)  
  
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    do k iters
      if  $\alpha = 0 \bmod 2$ 
         $\alpha <- \alpha / 2$  ; R <- record_halving_a(R)
      else
        if  $\alpha < \beta$  then
          swap( $\alpha$ ,  $\beta$ ) ; R <- record_swap(R)
           $\alpha <- (\alpha - \beta) / 2$  ; R <- record_subtraction(R)
    (a, b)                               <- batch_update(a, b, R, k)
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```

# Desynchronization

- (1) have  $a$ ,  $\alpha$  odd, and  $\alpha \geq \beta$ , but  $a < b$ .
- (2) have  $\alpha' := (\alpha - \beta)/2$ , and  $a' := (a-b)/2$ .
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if  $\alpha = 0 \bmod 2$ 
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if  $\alpha = 0 \bmod 2$             $\alpha$  is odd (else branch)
     $\alpha \leftarrow \alpha / 2$     $\alpha \geq \beta$ 
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$a$  is odd

$a < b$

$a \leftarrow a' (a < 0 !!)$

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$a$  is odd

```

 $a < b$ 
 $a \leftarrow a' \ (a < 0 !!)$ 

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$\alpha'$  is odd

$\alpha < \beta$  (swap)

$\alpha \leftarrow (\alpha - \beta)/2$

$a$  is odd

$a < b$

$a \leftarrow a' (a < 0 !!)$

$a'$  is odd

$a' < b$  (sync)

$a'' \leftarrow (b - a')/2$

but  $a' < 0$

$a'' = (b + |a'|)/2$

$a' < b + a'$

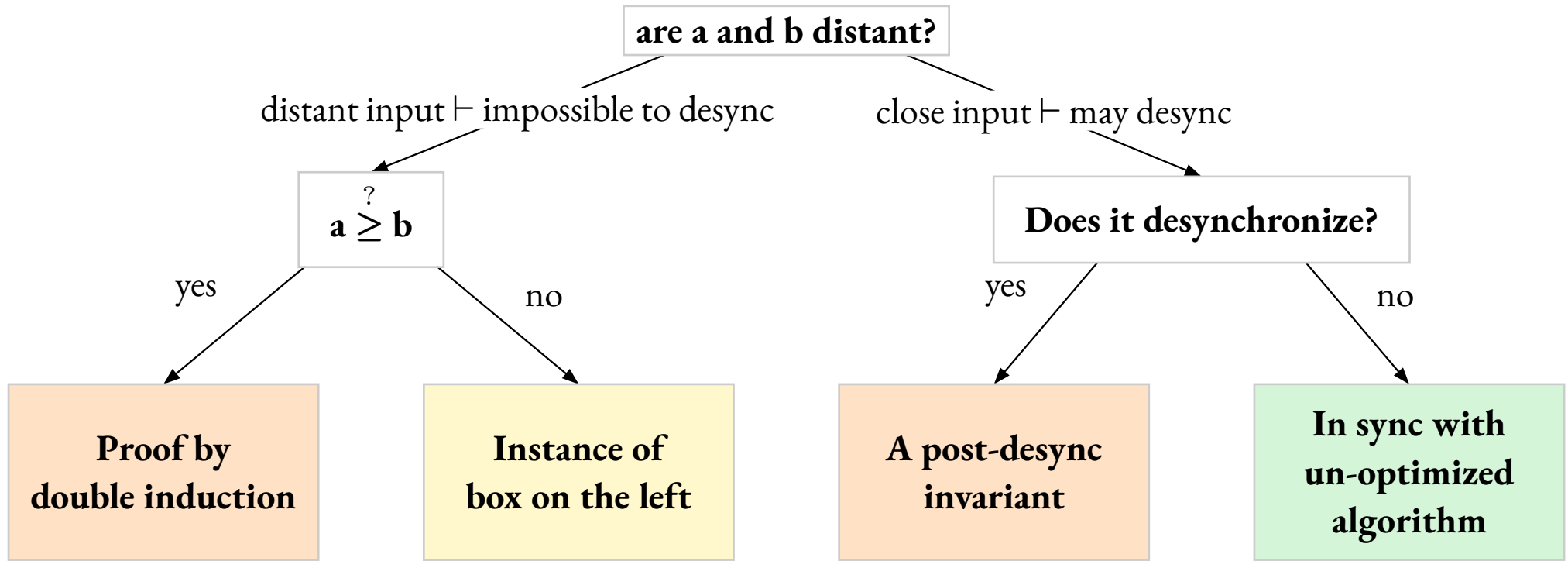
# Functional correctness

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# Why is using approximations correct?

Goal: show  $a$  converges to  $0$ , but after desynchronization  $a - b$  may increase.

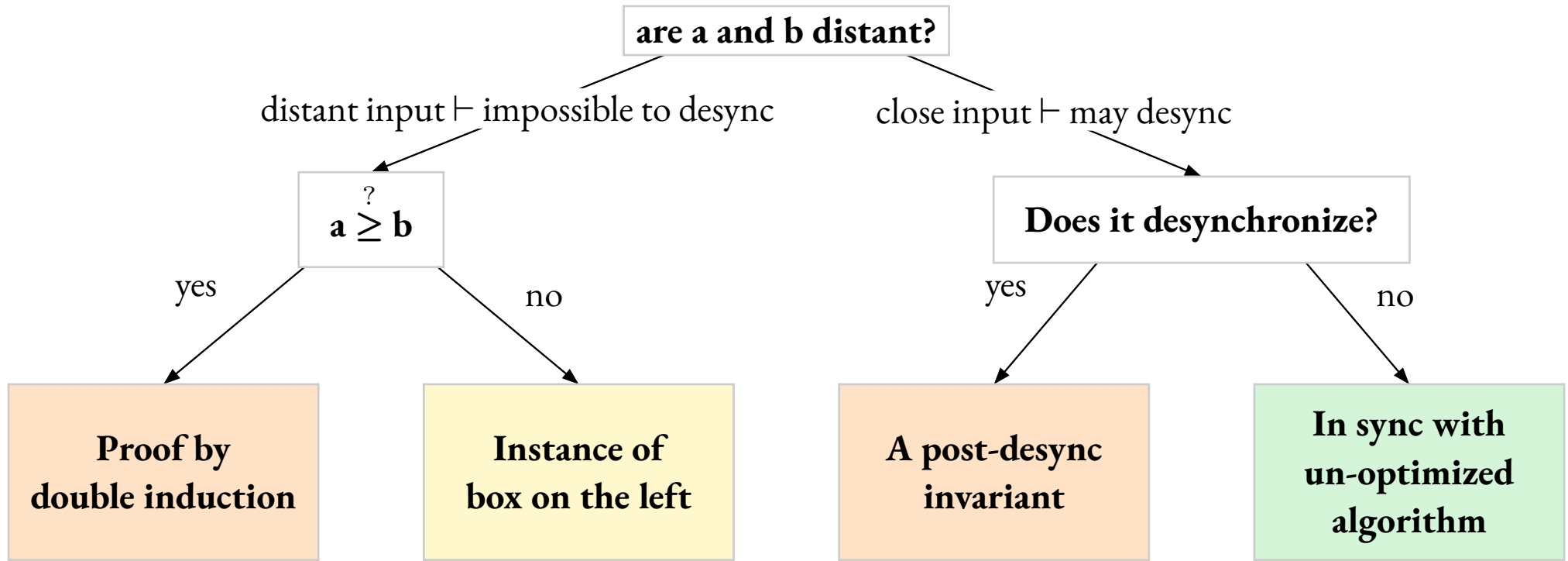
Strategy: establish a contract for the approximation loop. After  $k$  iterations at least  $k$  bits have been lost.



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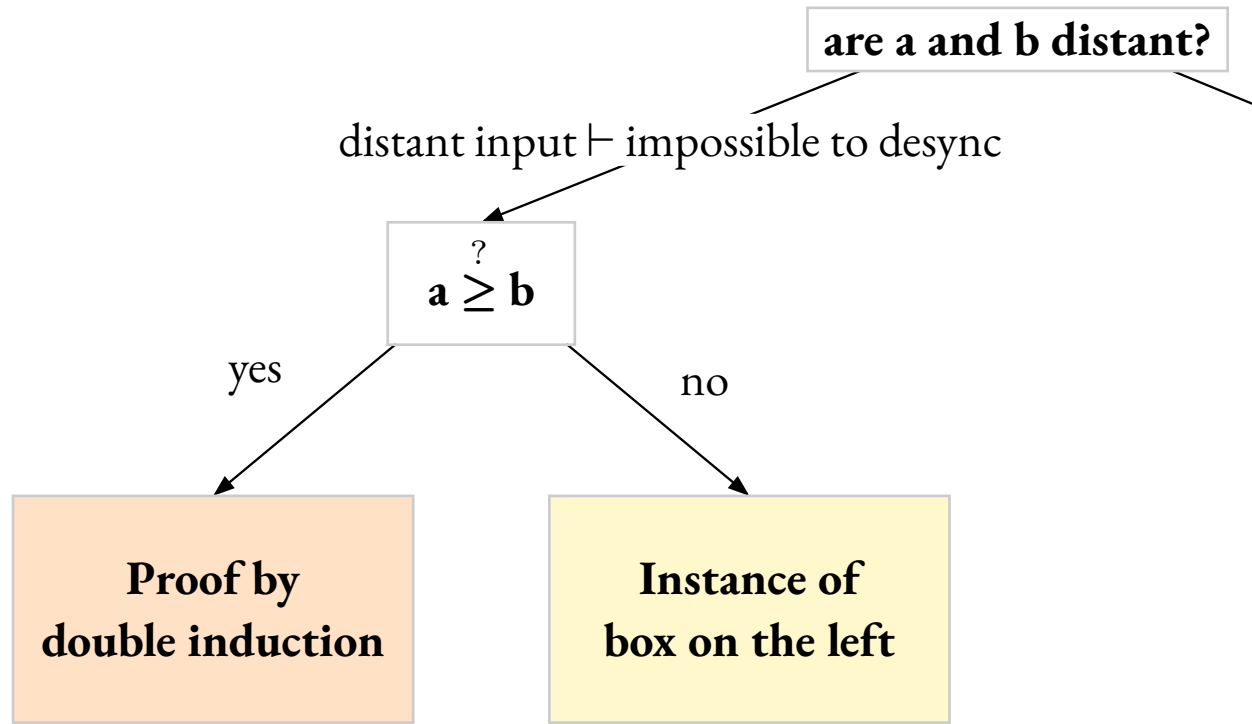


are  $a$  and  $b$  distant?

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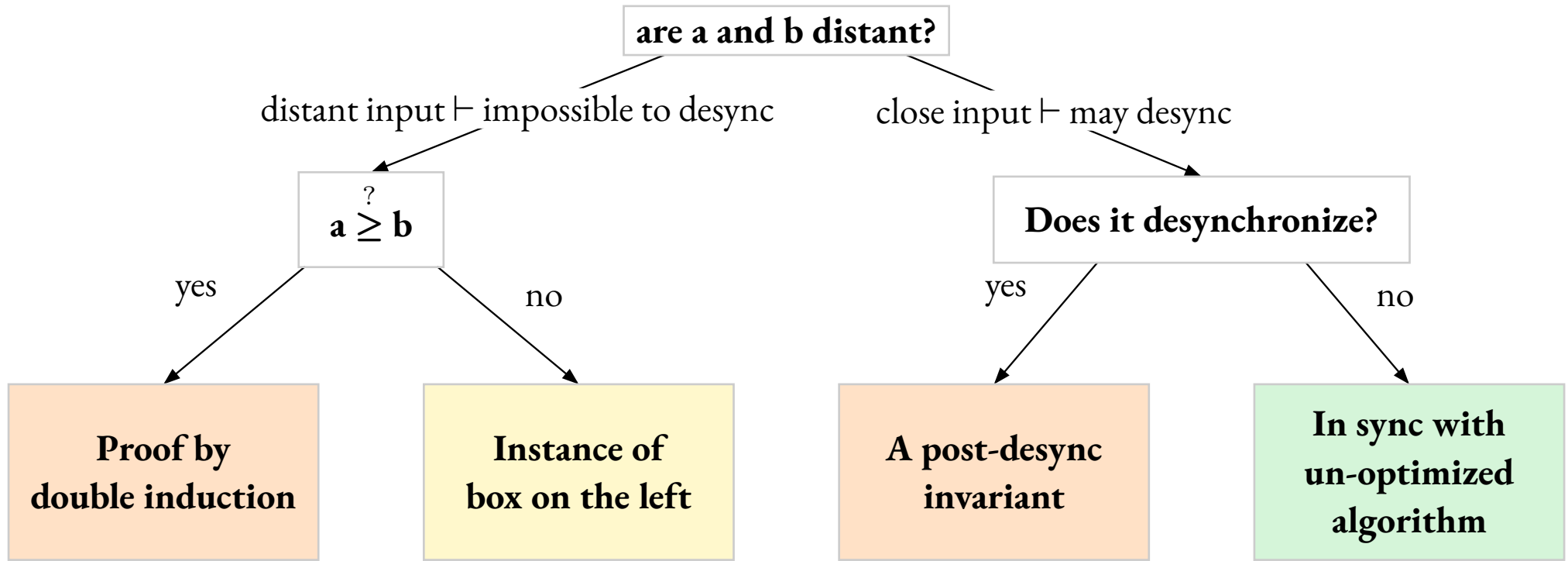
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# Desynchronization proof sketch

Let  $\ell(x)$  be the length in bits of  $x$ .

Under hypotheses:

- $\ell(a)$  and  $\ell(b)$  are close
- Desynchronization at iteration  $d$

Then, after  $k$  iterations  $\ell(a) + \ell(b)$  decreases at least  $k$  bits.

*Proof sketch.*

1. At iteration  $d$ , at least  $d$  bits were lost.
2. At iteration  $d + 1$ ,  $k$  bits were lost.
3.  $H(t) \triangleq P(t) \vee Q(t)$  is an invariant post-desync, and preserves  $k$  bits were lost.

With

- $P(t) \triangleq M_t \geq 2^{n-k+1} \wedge -2^{n-k} < m_t \leq 0$
- $Q(t) \triangleq \max(|a_t|, |b_t|) < 2^{n-k+1}$
- $M_t \triangleq \max(a_t, b_t) \quad \wedge \quad m_d \triangleq \min(a_t, b_t) \quad a_t \triangleq \text{batch\_update}(a, b, R, t) \quad b_t \triangleq \dots$

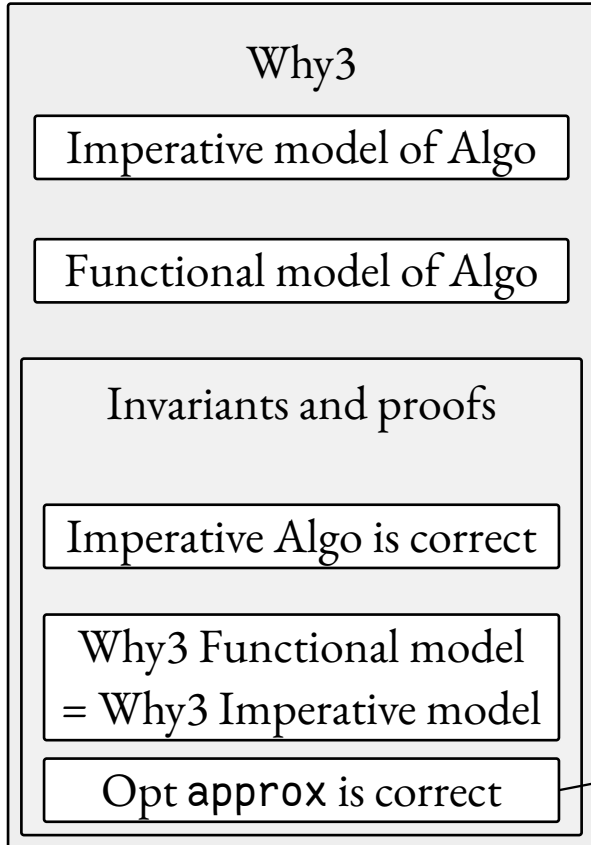
# Mechanization

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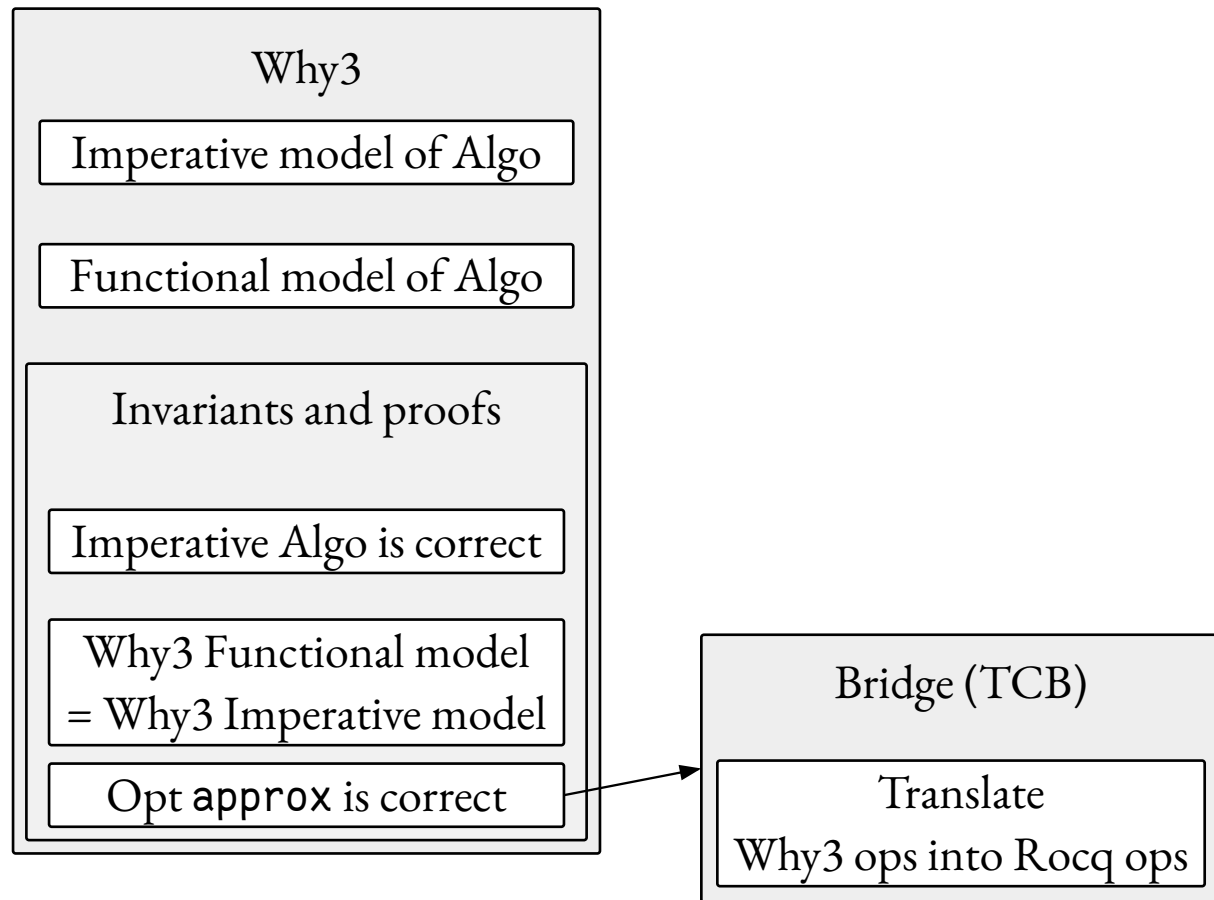
# Collaboration between Why3 and Rocq

- Initial plan: a complete formalization in Why3
- Implement the binary EGCD algorithm in Why3
- Iterative refinement of optimizations/proofs
- At approximation optimization:
  - Context too big for Why3+SMT to handle
  - Lack of expressivity to state and prove theorems
- Rocq model and proof that approximation optimization is correct
- Connect the proof via a bridge between Why3 and Rocq

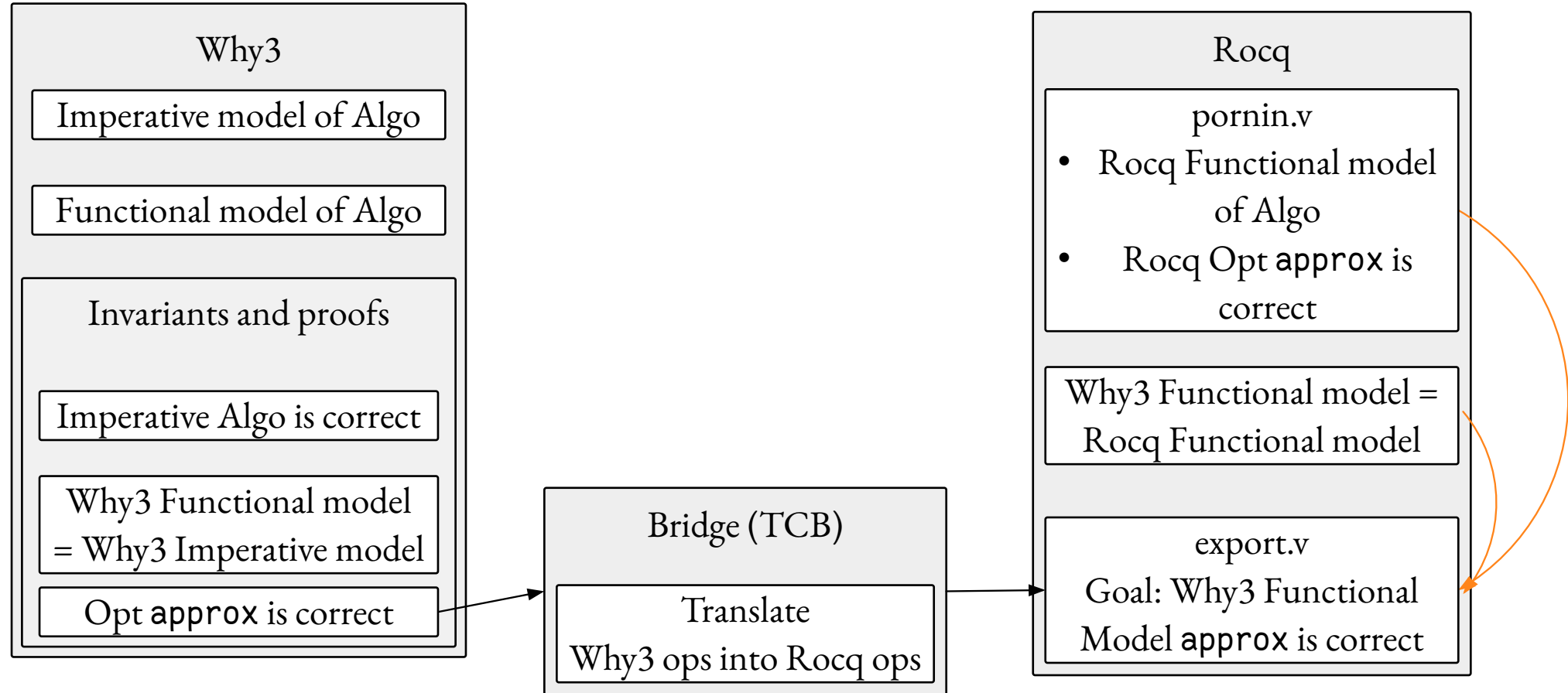
# Overview of the mechanization



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# Overview of the mechanization



## The bright side

- Near zero-effort spent glueing Rocq and Why3 models
- Some automation in Rocq for linear integer arithmetic
- No issue in mechanizing double induction arguments in Rocq

## The not so bright side

- SMT solvers were not helpful with non-linear arithmetic
- Floyd-Hoare logic was not suitable to deal with desynchronization proofs
- Tons of proof transferring from  $\mathbb{N}$  to  $\mathbb{Z}$  (e.g. lift  $e1 < e2$  from `nat` to `int`)

## Summary/Context

- Highly optimized algorithm used in critical security context
- Possibly incorrect functional correctness proof

## Contributions

- Complete functional correctness proof
- Mechanization of the functional correctness proof
- Verification of an imperative implementation of the algorithm

The current version of the algorithm, and the revised proof, are believed correct.



**ROCQ**  
**APPROVED**



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